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Application Number	Application Number 10/692,091	
Filing Date	October 23, 2003	
First Named Inventor	Hung, Eric	
Title	Memory Module Having Mirrored	
Art Unit	2824	
Examiner Name	Nguyen, Dang T.	
Attorney Docket Number	03-1236	

Lhereby revoke a	I previous powers of attorney g	iven in the ab	ove-ide	entified applica	ation.	· · · · · · · · · · · · · · · · · · ·	
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I am the:  Applicant/Inventor.  Assignee of record of the entire interest. See 37 CFR 3.71.  Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)							
SIGNATURE of Applicant or Assignee of Record							
Signature	-0-1-W				Date	11JAN 66	
Name	Timothy R. Croll				elephone	(408) 433-7472	
Title and Company Senior Corporate Counsel, IP, LSI Logic Corporation							
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.							
▼ Total of1 forms are submitted.							

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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## STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: <u>LSI Logic Corporation</u>	
Application No./Patent No./Control No.: 10/692,091	Filed/Issue Date: October 23, 2003
Entitled: Method Module Having Mirrored Placement of DRAM Integrat	ed Circuits Upon for a Four-Layer Printed Circuit Board
LSI Logic Corporation ,	a Corporation
states that it is:  1.   the assignee of the entire right, title, and interest; or	Type of Assignee: corporation, partnership, university, government agency, etc.)
<ol> <li>an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is</li> </ol>	%)
in the patent application/patent identified above by virtue of eithe	er:
A. An assignment from the inventor(s) of the patent applicatio in the United States Patent and Trademark Office at Reel _ original assignment is attached.  OR	n/patent identified above. The assignment was recorded 014657 , Frame 0788 , or a true copy of the
	on/patent identified above, to the current assignee as follows:
1. From: To: The document was recorded in the United States Page  , Frame,	atent and Trademark Office at
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Additional documents in the chain of title are listed on a	
As required by 37 CFR 3.73(b)(1)(i), the documentary evident assignee was, or concurrently is being, submitted for record [NOTE: A separate copy (i.e., a true copy of the original assi Division in accordance with 37 CFR Part 3, to record the 302.08]	dation pursuant to 37 CFR 3.11. ignment document(s)) must be submitted to Assignment
The undersigned (whose title is supplied below) is authorized to	act on behalf of the assignee.
Signature	Date
Timothy R. Croll	(408) 433-7472
Printed or Typed Name	Telephone Number
Senior Corporate Counsel, IP Title	

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